



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Fumiaki KARASAWA et al.

Application No.: 10/642,737

Docket No.: 116884

Filed: August 19, 2003

For: SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING THE SAME,
CIRCUIT BOARD AND ELECTRONIC APPARATUS

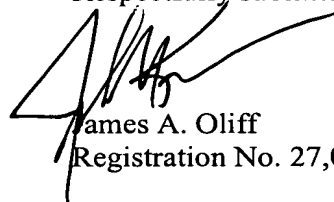
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



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Date: May 19, 2004

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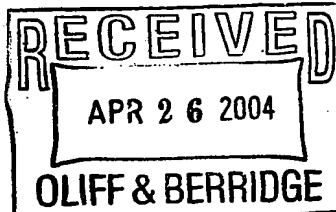


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/642,737	08/19/2003		1176	116884	7	1	1

25944
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CONFIRMATION NO. 3798

FILING RECEIPT



OC000000012416934

Date Mailed: 04/22/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-241827 08/22/2002

If Required, Foreign Filing License Granted: 04/21/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor apparatus and the manufacturing method, and circuit board and electronic device

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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